



S/N 09/510,375

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Brett L. Williams Examiner: Hong Kim
Serial No.: 09/510,375 Group Art Unit: 2185
Filed: February 22, 2000 Docket: 303.164US3
Title: SYSTEM SUPPORTING MULTIPLE MEMORY MODES INCLUDING A
BURST EXTENDED DATA OUT MODE

PRELIMINARY AMENDMENT

Box CPA
Commissioner for Patents
Washington, D.C. 20231

The Applicant has carefully reviewed the Office Action mailed on April 25, 2001 and timely files this response within two month of the mailing date. The Applicant requests the Examiner's consideration of the detailed comments found in the Remarks section below.

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Miscellaneous Observations

The Applicant notes the Examiner's observations regarding the instant application and prior Application Serial No. 08/386,894 (filed February 10, 1995, and now U.S. Patent No. 5,610,864), which is in turn a continuation-in-part of Application Serial No. 08/370,761 (filed December 23, 1994, and now U.S. Patent No. 5,526,320). The Applicant appreciates the Examiner's thorough review of these cited applications and declines to take advantage of any priority claim with respect them at this time.

REMARKS

Applicant has carefully reviewed and considered the Office Action mailed on April 25, 2001, and the references cited therewith.

No claims are amended, no claims are canceled, and no claims are added; as a result, claims 26-39 are now pending in this application.

§102 Rejection of the Claims

Claims 26, 29, 32 and 35-39 were rejected under 35 USC § 102(a) as being anticipated by "Intel" Electronic News, (EN) December 5, 1994 in view of 82430FX PCIset Datasheet